

To: Examiner Britt
Facsimile Number: 703-872-9306
From: Lawrence J. Bassuk
Texas Instruments Incorporated
Facsimile: 972-917-4418 / Phone: 972-917-5458

Total Pages Sent: **2069**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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MAY 20 2005

In re the Application of

Lee D. Whetsel

Serial No.: 10/691,225

Filed: 10/22/2003

For: Circuit With Expected Data Memory Coupled to Serial Input Lead

Docket Number: TI-14124D.6

Art Unit: 2133

Examiner: Cynthia H. Britt

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the
U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:

Lawrence J. Bassuk

May 20, 2005

Date

FACSIMILE COVER SHEET

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NAME OF INVENTOR(S): Lee D. Whetsel	
TITLE OF INVENTION: Circuit With Expected Data Memory Coupled to Serial Input Lead	
TI FILE NO.: TI-14124D.6	DEPOSIT ACCT. NO.: 20-0668
FAXED: May 20, 2005 DUE: July 29, 2005 ATTY/SECY: LJB/sg	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/691,225 Filing Date: 10/22/2003	

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75074

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In re the application of
Whetsel

TI-14124D.6

Application No. 10/691,225

Art Unit: 2133

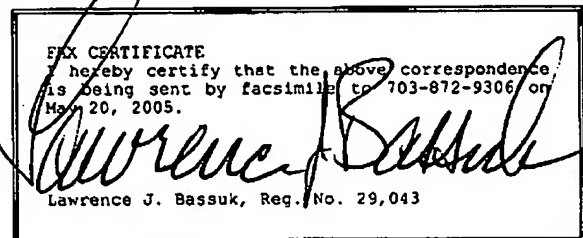
Filed: 10/22/2003

Examiner: Britt, Cynthia

Title: Circuit With Expected Data Memory Coupled to Serial Input Lead

May 20, 2005

Asst. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313



Dear Sir:

Transmitted herewith is an amendment in this application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	57	Minus	57	= 0	x \$50 =	\$ 0
Ind. Claims	3	Minus	3	= 0	x \$200 =	\$ 0
TOTAL ADDITIONAL FEE FOR THIS AMOUNT						\$ 0

Under 37 C.F.R. § 1.16(k) please charge the total additional fee, and any further fees, or credit overpayment to Deposit Account No. 20-0668, of Texas Instruments Incorporated.

Respectfully submitted,

Lawrence J. Bassuk
Reg. No. 29,043
Attorney for Applicant

Texas Instruments Incorporated
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Whetsel

TI-14124D.6

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Application No. 10/691,225

Art Unit: 2133

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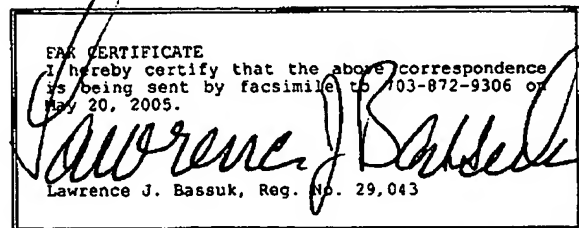
Examiner: Britt, Cynthia

Title: Circuit With Expected Data Memory Coupled to Serial
Input Lead

Amendment B Under 37 CFR 1.111

May 20, 2005

Asst. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313



Dear Sir:

Responsive to the Examiner's Action of 4/21/2005, applicant
responds as follows: